

**Yashavantrao Chavan Institute of Science, Satara (Autonomous)**

**Faculty Profile**

**1. Personal Information:**

- (a) Name **Mr. Sushant Ramchandra Pol**
- (b) Date of Birth 05<sup>th</sup> December 1988
- (c) Address (Resident) At:BalupatlachiWadi,  
Po: Lonand, Tal: Khandala,  
Dist: Satara, Maharashtra  
Pin: 415521  
Mobile: +91-9730951806  
E-mail: [srp@ycis.ac.in](mailto:srp@ycis.ac.in)
- (d) Designation Assistant Professor
- (e) Department Electronics
- (f) Date of Appointment 17/02/2020
- (g) Area of specialization Embedded System, Internet of Things,  
Artificial Intelligence



**2. Academic Qualification:**

Examination	Board/University	Year of passing	% Marks	Class
M. Sc.	Pune University	2011	65.30	First
B. Sc.	Shivaji University	2009	65.04	First
H.S.C	Kolhapur Board	2006	61.67	First
S.S.C	Kolhapur Board	2004	68.00	First
PG-DESD	C-DAC Hyderabad	2012	58.24	Second
NET	University Grants Commission	2013	66.86	First
SET	Savitribai Phule Pune University	2014	62.86	First (3 <sup>rd</sup> Rank)

**3. Teaching Experience:(PG: 01 Year)**

Since 17/02/2020 as Assistant Professor at Department of Electronics,  
Yashavantrao Chavan Institute of Science, (Autonomous) Satara

**4. Positions Held:**

Designation	From	To	Name of Institute
<b>Coordinator:</b> B.Sc. Computer Science	04/09/2017	26/04/2019	YCIS, Satara
<b>Coordinator:</b> M.Sc. Electronics	26/04/2019	Till Date	YCIS, Satara

**5. No. of Research Publications: 03**

- In National/International Journals: **01**
- In National /International Conference Proceedings: **02**

**6. Patents: 02 (Applied) 01 (Drafted)****7. Papers Presented at National/International Conferences:05****8. Number of Seminars/conferences attended: 23**

	Conference	Workshop
International	02	02
National	03	03
State/Regional	00	12

**9. Particulars of Orientation / Faculty Development courses attended: 13**

Year	Details of Course
2014	1. On Board Training: Pt-51, Aurum and Krypton, 22 <sup>nd</sup> August 2014, e-prayog, IIT Bombay
2016	1. International Winter Course on Organic Solar Cells: Principles and Practices, 2-9 November 2016, NIT, Calicut
	2. Light Emitting Nanomaterials and their Applications in Energy Related Fields, 22-28 December 2016, AMU, Aligarh
2017	1. Indo-German Teachers Program on Organic Electronics, 06 <sup>th</sup> December 2017, TIFR, Mumbai
2018	1. INUP Familiarization Workshop on Nanofabrication Technologies, 14-16 March 2018, IISc, Bangalore
	2. Teaching and Learning Center for Internet of Things, 24-28 September 2018, IIT Patna
2019	1. Faculty Development Program in Entrepreneurship, 28 January – 07 February 2019, from NSTEDB (Govt. of India), STP
	2. Faculty Development Program “Recent Advances in UG Programme”, 11-17 June 2019, YCIS Satara, by RUSA,
	3. Workshop Introduction to Robotics, 15-16 March 2019, IIT Bombay
	4. e-Yantra Task Based Training (TBT), 17 <sup>th</sup> May – 26 July 2019, IIT Bombay
	5. e-Yantra Task Based Training (TBT): e-Yantra lab Setting Initiative from IIT Bombay
2020	1. Orientation Program for "Faculty in Universities/Colleges/Institutes of Higher Education by Ramanujan College, Delhi, Sponsored by MHRD, June 04 - July 01, 2020
	6. MSME SkillTechnika Government Certified Online Industrial Training for College Faculties on Artificial Intelligence (AI) & Internet of Things (IoT), April 19 - May 09, 2021

## **10. Individual Projects Completed: 01**

## **11. Membership/participation in Curricular activities:**

- **Member**, Draft Syllabus Committee in Electronics of Yashwantrao Chavan Institute of Science (Autonomous), Satara, for P.G. and U.G. syllabus.
- **Resource Person**, University Level REVISED SYLLABUS WORKSHOP for B.Sc. III Electronics, 13<sup>th</sup> August 2015
- Participated in University Level REVISED SYLLABUS WORKSHOP for B.Sc. III Electronics, 23<sup>rd</sup> August 2015 and 12<sup>th</sup> September 2015

## **12. Invited Talks: 07**

- (a) **Resource Person**, “Recent Advances in Embedded Systems” under Lead College Activity 21<sup>st</sup> January 2016, S. P. Mahavidyalaya Lonand
- (b) **Evaluator** SUSTA Quiz competition, 31<sup>st</sup> January 2016, YCIS, Satara
- (c) **Resource Person**, SET/NET Examination Guidance Workshop, 15<sup>th</sup> March 2017, YCIS, Satara
- (d) **Resource Person**, Centenary Program of Rayat Shikshan Sanstha for Students Workshop on Innovations Basic, 10<sup>th</sup> October 2018, Dahiwadi College, Dahiwadi
- (e) **Resource Person**, Rayat Inspire Camp, 29<sup>th</sup> November 2018, Dahiwadi College, Dahiwadi
- (f) **Designed Practical Boards** for Department of Physics, Dahiwadi College, Dahiwadi
- (g) **Resource Person**, Workshop on Model Based Learning, 03<sup>rd</sup> October 2019, Dahiwadi College, Dahiwadi

## **13. Conveyed Conferences Workshops: 05**

- **Coordinator**, International Conference on Emerging Trends and Challenges in Electronics and Computing (ETEC2021), 4-6 March 2021
- **Organizing Secretary**, NATIONAL CONFERENCE on "Innovative Trends in Electronics and Allied Technology (ITEAT-2017), February 2017, YCIS, Satara
- **Treasurer**, NATIONAL CONFERENCE on "Modern Approach for Green Electronics and Computing (MAGEC-2014), September 2014, YCIS, Satara
- **Editor**, PROCEEDINGS "Modern Approach for Green Electronics and Computing (MAGEC-2014), ISBN: 978-81-928732-2-0.
- **Editor**, SOUVENIR National Conference on "Innovative Trends in Electronics and Allied Technology (ITEAT-2017), February 2017, YCIS, Satara
- **Organizing committee member**, National Workshop on “Embedded System Designed based on 8051 & PIC microcontroller” 12-13 February 2016, YCIS, Satara

## **14. Membership of Professional bodies, Societies etc.**

- (a) **Life Member**: Society for Promotion of Excellence in Electronics Discipline (SPEED)
- (b) **Member**: International Association of Engineers (IAENG)
- (c) **Associate member**: Universal Association of Computer and Electronics Engineers
- (d) **Annual Member** Indian Science Congress

## **15. Specific Contribution to teaching / Academic environment / Institutional Corporate Life:**

### **➤ Teaching:**

#### **(a) Design of curriculum:**

- Member Board of Studies; contributed in design of curricula in Electronics for Yashavantrao Chavan Institute of Science (Autonomous), Satara
- Developed and implemented Curricula for Certificate, Diploma and Advance Diploma courses in Embedded System Design, Internet of Applications.

#### **(b) Teaching methods:**

- New methods evolved and experimented in teaching such as Brainstorming, Introduction of Computer Assisted Learning.
- Extensive use of Power point presentations in regular teaching
- Development of e-contents for multimedia web based courses.

#### **(c) Laboratory Experiments:**

- New laboratory experiments were set up (As per Curricula)
- Design of special kits for Electronics Lab e.g. Active components tester kit
- Microcontroller development systems and tools
- Evolution of a custom training model for C programming lab

#### **(d) Evaluation methods and works:**

- Student capability feedback on aptitude, problem solving and proficiency.
- Paper Setter for B.Sc. III, M.Sc. I and M.Sc. II examination
- Examiner for B.Sc. III Electronics practical examination in SP College, Lonand, in April 2019
- Examiner for B.Sc. III Electronics practical examination in SGM College, Karad and SP Mahavidyalaya Lonand, in April 2016
- Central Assessment work at B. Sc. I, II and III
- Junior Supervisor for UG and PG Examinations at YCIS, Satara.

#### **(e) Preparation of resources :**

- Prepared laboratory manuals and course material.
- Material including e-books, lab manuals for M.Sc. as well as various value added Certificate courses
- Contributed in Preparation of e-content

#### **(f) Remedial teaching /Student Counseling:**

- Participated in academic social counseling for students
- Organized and conducted Internship Preconditioning workshops
- Organized Alumni meet

### **➤ Institutional:**

- **Resource Person** for Five Day Rayat INSPIRE CAMP (High School, Junior College) at YCIS, Satara (Every year from 2016)

- **Member**Tech Support Committee in Rayat Shikshan Sanstha's "KARMAVEER VIDYA PRABODHINI QUIZE COMPETITION"
- **Member**,Rayat Shikshan Sanstha's SCIENCE INNOVATION CENTRE Exhibits Designing Committee – Electronics Section
- **Member**,Mutha XEV Program to convert EV a fleet of TATA Ace and Two Wheeler
- CommunicatedPaper-MATLAB Programming to **M.Sc. II Physics** (2015-2019)
- CommunicatedPaper-Basic Electronics to **B.Sc. I Nanoscience and Technology** (2013-14)

➤ **Academic Environment:**

- Actively working with Institutions, science and engineering colleges as well as Industries for creating a harmonious teaching learning environment.
- Organized Workshops and National Seminar for Promotion of Basic Science as a career.
- Delivered talks in English as well as Marathi on popularization of Science at college and school level.
- Contributed as a resource person in the Science Exploratory for school children

➤ **Corporatelife:**

- Participatedan informal Academic forum of Electronics teachers of Shivaji University, Kolhapur (SUK Electronics) and Savitribai Phule Pune University Pune (Electronics Association)

**16. Publications and Presentations:**

**2021:**

1. "Simulations of Various Memristors, Models, Characteristic and Its Potential Applications" S. R. Pol, S. K. Shinde, A. A. Mulla, S. H. Mujawar, International Conference on Emerging Trends and Challenges in Electronics and Computing, 4-6 March 2021

**2019:**

1. "Internet of Things (IoT) Based Portable Biometric Attendance System",S. R. Pol, R. P. Waghmare, J. A. WaghInternational Conference on Chemistry, Energy and Environment (ICCEE-2019), YCIS, Satara. ISBN: 978-91-928732-7-5

**2018:**

1. "Internet of Things for Renewable Energy: Review", S. K. Shinde, S. R. Pol, J. A. Wagh, B. T. Jadhav, International Conference on Recent Advances in Renewable Energy and Hygiene Devices (RAREHD-2018), YCIS, Satara.
2. "The Fabrication of DSSCs using TiO<sub>2</sub> and Natural Dyes",S. R. Pol, S. K. Shinde, P. K. Pagare, A. J. Pawar, S. A. Shinde, B. T. Jadhav, J. A. WaghInternational Conference on Recent Advances in Renewable Energy and Hygiene Devices (RAREHD-2018), YCIS, Satara.

**2017:**

1. “Conducting Polymers and Applications in Organic Electronic Devices”, **S. R. Pol**, National Conference on "Innovative Trends in Electronics and Allied Technology (ITEAT-2017), February 2017, YCIS, Satara

**2014:**

1. “MATLAB Based LED Matrix Scrolling Display Powered by Solar Energy”, **S. R. Pol**, P. M. Kalekar, A. J. Wagh, R. A. Nanaware, V. S. Kale, MAGEC-2014, Satara, ISBN : 978-81-928732-2-0.
2. “Wavelet Transform Based Techniques for Fault Detection of Power Supply Unit”, J. A. Wagh, S. K. Shinde, **S. R. Pol**, R. R. Mudholkar, P. P. Shah, IJEE Volume-6, Number-1, Jan-June 2014, pp.47-50 ISSN: 0973-7383.
3. “Simulation Model for Wireless Sensor Network: A MATLAB Approach”, **S. R. Pol**, National Conference on Advances in Wireless Sensor Network and its Applications (NCAWSNA-2014), SMM, Akhuj

**2013:**

1. “A Design and Implementation of Temperature Monitoring System”, A. J. Pawar, **S. R. Pol**, A. J. Wagh, ICIASP-2013, Karad, ISBN 978-81-8487-359-7.