

**Report**  
**“A One Day Workshop on Draft Syllabus for B.Sc.I and M.Sc.I”**  
**16<sup>th</sup> Dec ,2017**

Department of Electronics had organised “A One Day Workshop on Draft Syllabus for B.Sc-I and M.Sc-I” on Saturday 16<sup>th</sup> December 2017 at 10.30am in the department of Electronics. The workshop began with a welcome speech by Mr. J. A. Wagh, Coordinator of workshop and he has explained the theme of the workshop and inaugurated by chief guest Mr. Yogesh Shinde, (Ex Senior Scientist ISRO and Lead engineer EATON Technology,Pune ) in the presence of Vice Principal Dr. H. V. Deshmukh, Dr. H. P. Umap and Dr. Santosh Shinde.

The key concept of the workshop was to discuss the core structure for autonomous draft syllabus under CBCS and to made the syllabi recent, research , industry and job oriented . The workshop is conducted in the four sessions. In this session we had thoroughly discussed theory and lab courses for B.Sc.-I and M.Sc.- I under autonomous status. We had incorporated the new concept, recent advances which will be supported for industry and research. 15 Departmental faculty had been participated in this workshop the overall output of the workshop was very satisfactory and gain oriented. The workshop had concluded by valedictory function.