# Rayat Shikshan Sanstha's Yashavantrao Chavan Institute of Science, Satara (Autonomous)

# Department of Electronics

# I Year Diploma Practical Examination Time Table

Date: - 01/04/2024

All students of B.Sc-I, Sem-II here by inform that end semester Diploma practical examination will be conducted as per the following time table. All students should attend the practical as per following time table.

Date	Time	Batch No.	Hall No	Roll Number	Paper
Monday 29/04/2024	8.00 to 11.00	Ι	B.Sc I Practical laboratory I	703,711, 34 to 64	DEP 202
	11.00 to 2.00	II	B.Sc I Practical laboratory I	719, 65 to 86	DEP 202

#### Instruction:

1. Practical exam will be carry 40 marks.

2. There will be total 2 questions each question carries 30 marks students should attempt any one out of them..

3. Be present in examination hall before 15 min

# Practical examination marks rubrics as follows.

Paper	PDISE	Experiment	Attendance	lab work	Project Report and Viva	Total
DEP 202	30 Marks	30 Marks	05 Marks	10 Marks	25 Marks	100 Marks

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#### Rayat Shikshan Sanstha's

# Yashavantrao Chavan Institute of Science, Satara (Autonomous)

### **Department of Electronics**

Second Year Diploma Program, Sem-IV, May-2024

## **Diploma Practical Examination Time Table**

Date: - 01/04/2024

All students of the Second Year Diploma Program, Sem-IV here to inform you that the end semester Diploma practical examination will be conducted as per the following time table. All students should attend the practical as per the following time table.

Date	Batch No.	Hall No	Roll Number	8.00 am to 11.00 am	11.00 am to 2.00 pm
Sunday 05/05/2024	Ι	B.Sc II Practical laboratory	2047, 2052 to 2057, 2059, 2061 to 2065, 2121 to 2122, 2142	Practical Exam	Project report and viva
	II	B.Sc II Practical laboratory	2126,2131,2134,2136,2153 to 2154, 2157 to 2159,2161 to 2162, 2164 to 2165, 2392, 2397, 2401	Project report and viva	Practical Exam

#### Instruction:

- 1. The practical exam will carry 100 marks.
- 2. There will be a total of 2 questions each question carries 30 marks students should attempt any one out of them.
- 3. Be present in the examination hall before 15 min

#### Practical examination marks rubrics are as follows.

	PDISE	Experiment	Attendance	lab work	Project Report and Viva	Total
Marks	30	30	05	10	25	100

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# Rayat Shikshan Sanstha's Yashavantrao Chavan Institute of Science, Satara (Autonomous) Department of Electronics

## III Year Diploma Practical Examination Time Table Title: Embedded System Design

Date: -01/04/2024

All students of B.Sc-III, Sem-VI here by inform that end semester Diploma practical examination will be conducted as per the following time table. All students should attend the practical as per following time table.

Date	Batch No.	Hall No	Roll Number	11.00 to 2.00	2.00 to 5.00
Monday 29/04/2024	I	B.Sc. III Practical laboratory	1 to 24	Practical Exam	Project report and viva

#### Instruction:

- 1. Practical exam will be carry 40 marks.
- 2. There will be total 1 expriment each question carries 30 marks students should attempt any one out of them..
- 3. Be present in examination hall before 15 min

#### Practical examination marks rubrics as follows.

	PDISE	Attendance	Experiment	lab work	Project Report and Viva	Total
Marks	30	05	30	10	100	175

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